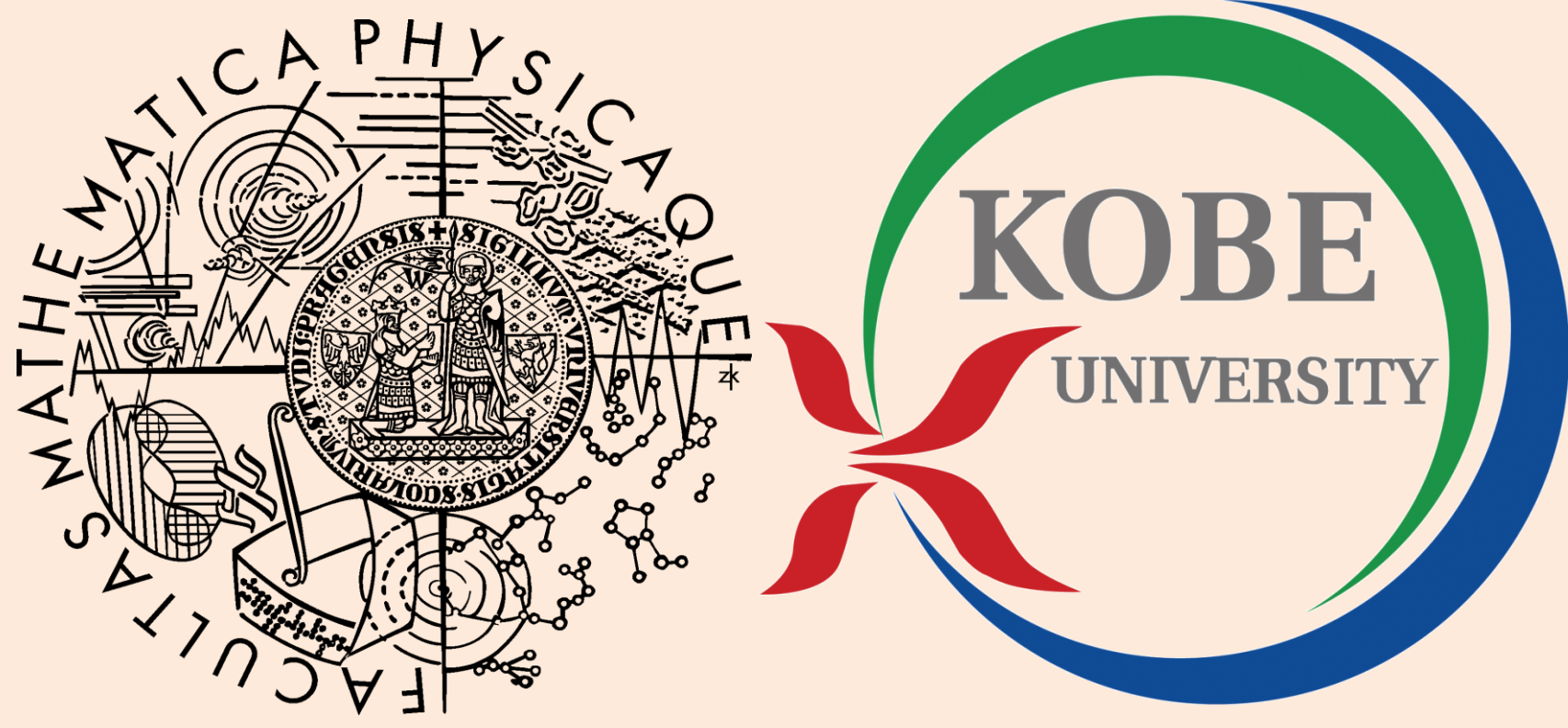


An Alternative Eager Encoding of the All-Different Constraint over Bit-Vectors



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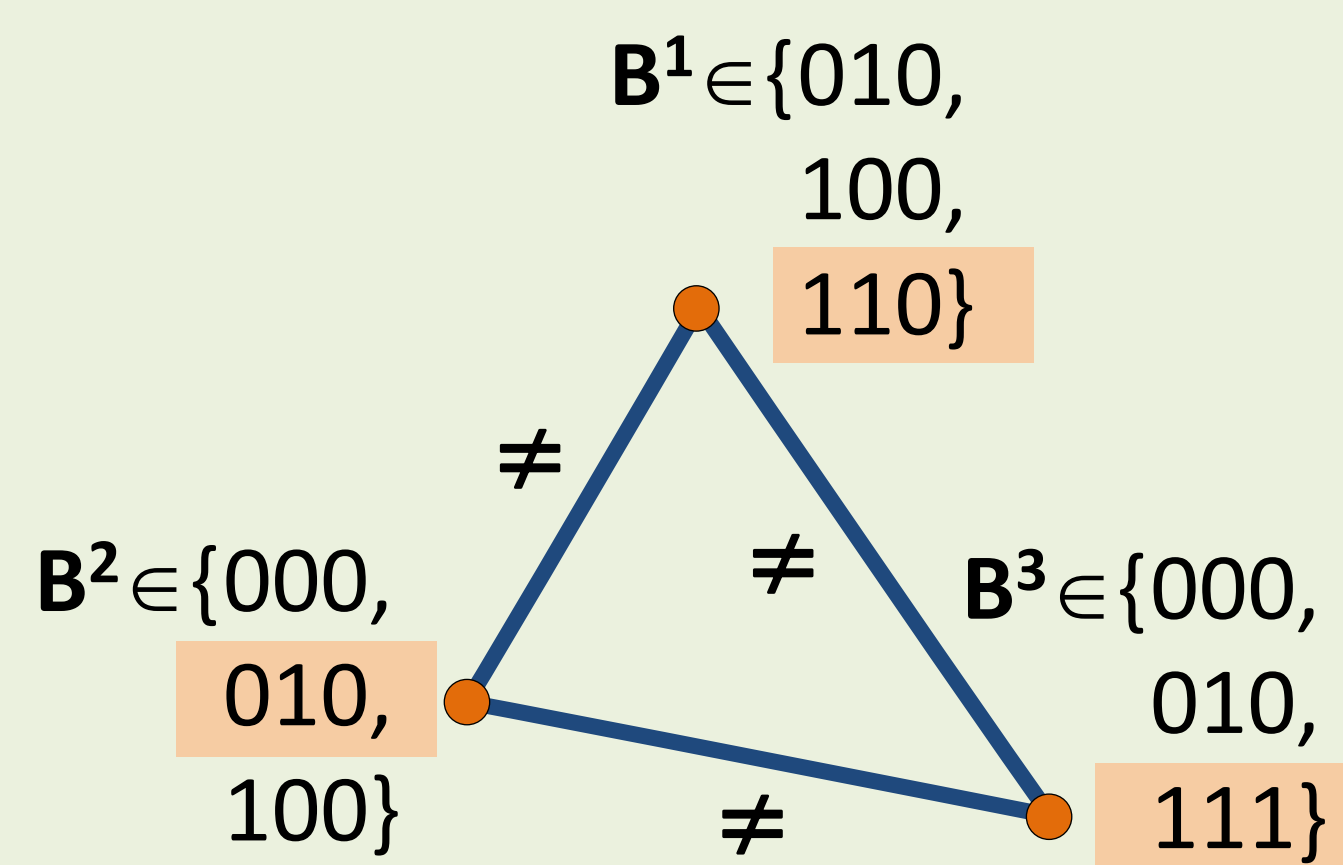
Graduate School of Maritime Sciences, Kobe University,
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All-Different over Bit-Vectors

- **All-Different** constraint
 - a **finite** set of variables with **finite** domain
 - each variable should be **assigned** a value from its domain
 - all the assigned values should be **distinct**
- Special case with **bit-vectors**
 - the involved variables are bit-vectors of fixed length $l \in \mathbb{N}$
 - domains consist of subsets of possible bit assignments

Example:

- **All-Different**(B^1, B^2, B^3)
- bit-vectors B^1, B^2 , and B^3
 - length = 3
 - different domains



- Use in the **SAT solving** technology

Theoretical Comparison

- **Standard model**
 - $l \cdot n$ visible propositional variables, $l \cdot n(n+1)/2$ auxiliary
 - $1 + l \cdot n(n+1)$ clauses
- **Alternative model**
 - $2l \cdot n$ visible, $2n \lceil \log_2 n \rceil$ bijection, $n^2 + l(n-1)$ auxiliary
 - $n^2(1 + l + \lceil \log_2 n \rceil) + l \cdot (l+1)(n-1)$ clauses

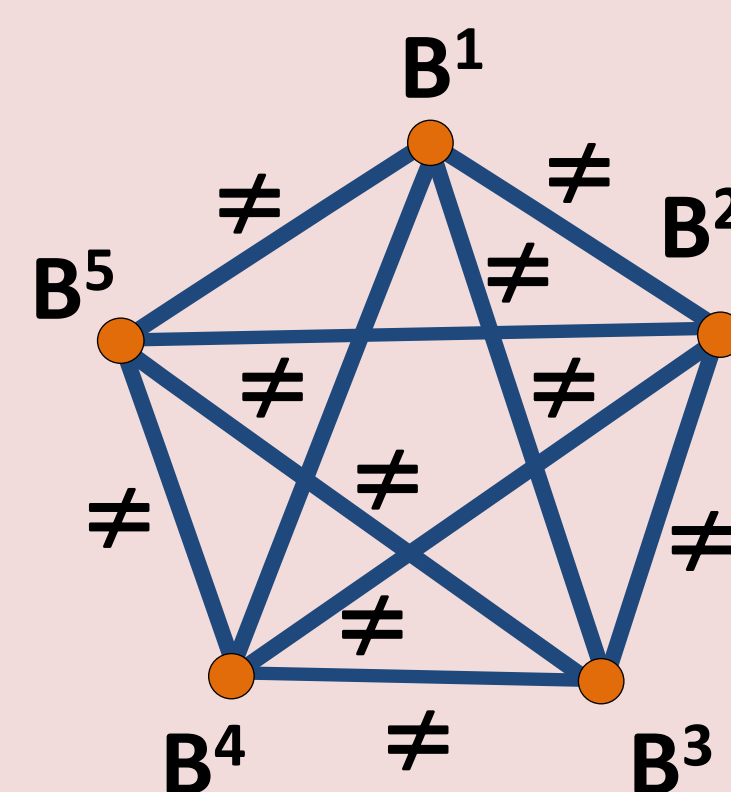
Model Size Evaluation

#bit-vectors (16-bits)	Standard		Alternative	
	#Variables	#Clauses	#Variables	#Clauses
64	67584	133056	9968	176943
128	266240	536448	28400	690031
256	1056768	2154240	90096	2756591

The Standard Model

- **All-Different**(B^1, B^2, \dots, B^n) $\equiv \bigwedge_{i,j=1; i < j}^n B^i \neq B^j$

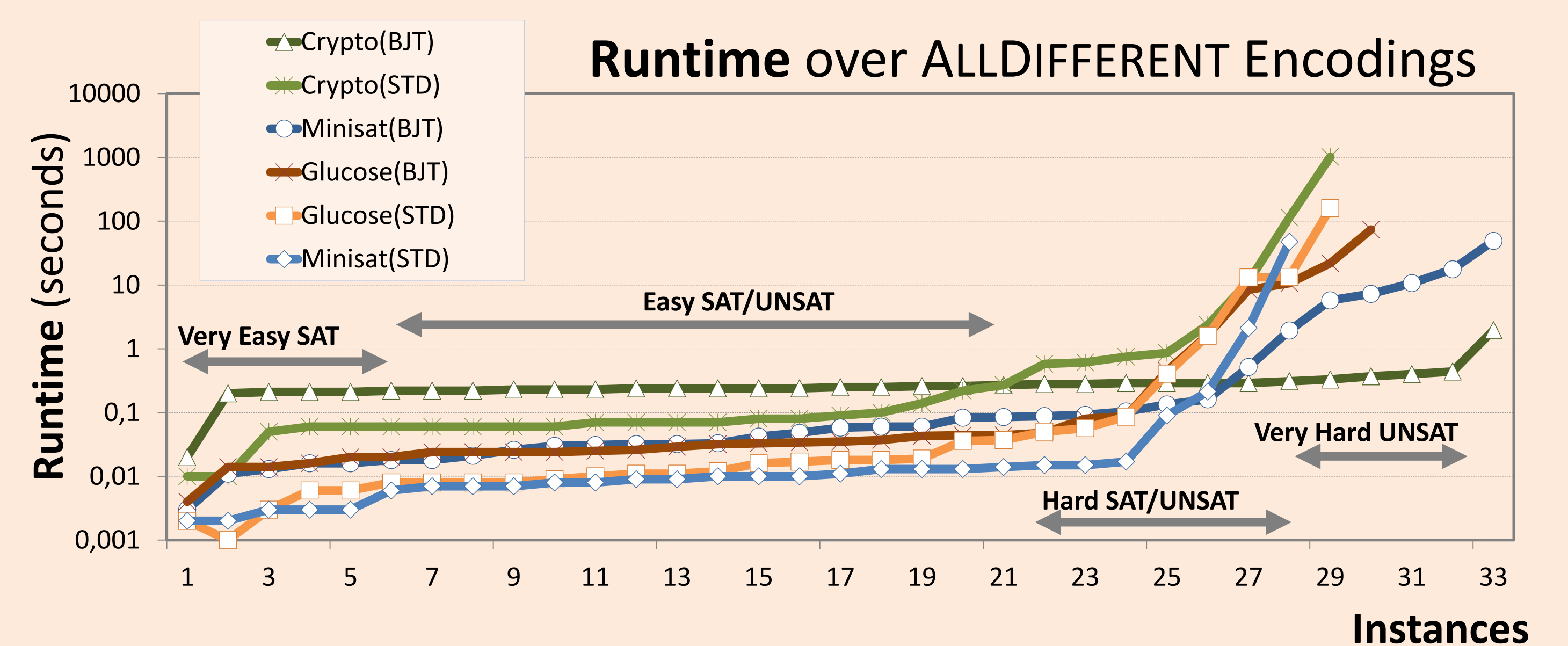
- **pair-wise differences** are encoded
- a single difference between two bit-vectors
 - $B^i \neq B^j \equiv \bigvee_{k=1}^l (\neg B^i_k \vee B^j_k) \wedge (B^i_k \vee \neg B^j_k)$



- **Auxiliary variables** are needed to translate the formula into CNF efficiently

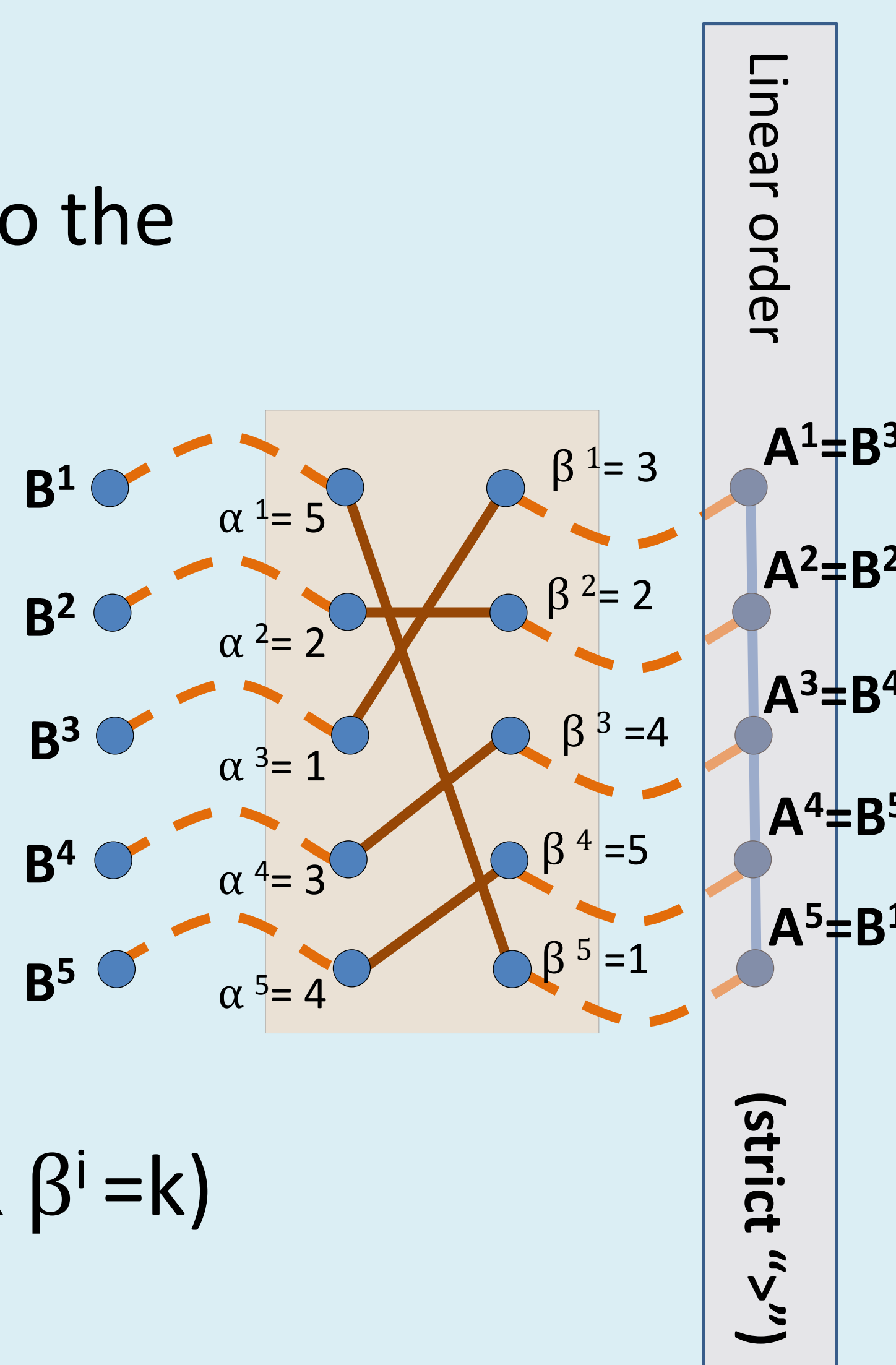
Runtime Evaluation

- **Setup**
 - 32 bit-vectors of length l , domain random interval of size up to 34, single all-different constraint



A New Alternative Model

- Introduce certain kind of **anti-symmetry** into the model
 - allow a SAT solver to **quickly discover** unsatisfiability
- **Map** the original bit-vectors to a **linearly ordered** set of auxiliary bit-vectors
 - A^1, A^2, \dots, A^n linearly ordered bit-vectors of length l
 - $\alpha^1, \alpha^2, \dots, \alpha^n$ auxiliary bit-vectors of length $\lceil \log_2 n \rceil$
 - α^i determines what A^i the original B^i is mapped to
 - $\beta^1, \beta^2, \dots, \beta^n$ auxiliary, length $\lceil \log_2 n \rceil$
 - ensure that at most one original bit-vector is mapped to a single ordered bit-vector
- **Bijection** constraint $\equiv \bigwedge_{i,k=1}^n \alpha^k = i \Rightarrow (B^k = A^i \wedge \beta^i = k)$
- **Linear order** constraint $\equiv \bigwedge_{i=1}^{n-1} A^i < A^{i+1}$



Conclusions

- **Pros and cons** of the alternative model
 - **fewer** variables
 - **more** constraints
 - **better** for discovering unsatisfiability in the hard case
 - **big overhead** in the easy case
- **Observation**
 - prominent solver sensitivity
- **Future works**
 - lazy integration with the SMT solver
 - evaluation in applications